Taiwan Semiconductor

## 1A, 50V - 1000V Standard Surface Mount Rectifier

### FEATURES

- AEC-Q101 qualified
- Glass passivated chip junction
- Ideal for automated placement
- Low-profile package
- Low power loss, high efficiency
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

## APPLICATIONS

- DC to DC converter
- Automotive application
- Car lighting
- Snubber
- General purpose

## MECHANICAL DATA

- Case: Sub SMA
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.019g (approximately)

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)									
PARAMETER	SYMBOL	S1AL H	S1BL H	S1DL H	S1GL H	S1JL H	S1KL H	S1ML H	UNIT
Marking code on the device		1AL	1BL	1DL	1GL	1JL	1KL	1ML	
Repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	35	70	140	280	420	560	700	V
Forward current	I <sub>F</sub>				1				Α
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	I <sub>FSM</sub> 30			A				
Junction temperature	TJ	J - 55 to +175			°C				
Storage temperature	T <sub>STG</sub>	- 55 to +175			°C				

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
١ <sub>F</sub>	1	А		
V <sub>RRM</sub>	50 - 1000	V		
I <sub>FSM</sub>	30	А		
T <sub>J MAX</sub>	175	°C		
Package Sub SMA				
Configuration Single die				
(Ph) Ro		EN		



Anode

Cathode -





THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-lead thermal resistance <sup>(1)</sup>	R <sub>θJL</sub>	25	°C/W
Junction-to-lead thermal resistance <sup>(2)</sup>	R <sub>θJL</sub>	30	°C/W
Junction-to-ambient thermal resistance	R <sub>ØJA</sub>	85	°C/W

Notes:

1. Part number: S1ALH - S1JLH

2. Part number: S1KLH - S1MLH

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage <sup>(1)</sup>	I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C	V <sub>F</sub>	-	1.1	V
Reverse current @ rated $V_R^{(2)}$	$T_J = 25^{\circ}C$	- I <sub>R</sub>	-	5	μA
	$T_J = 125^{\circ}C$		-	50	μA
Junction capacitance	1MHz, $V_R = 4.0V$	CJ	9	-	pF
Reverse recovery time	$I_F = 0.5A, I_R = 1.0A,$ $I_{rr} = 0.25A$	t <sub>rr</sub>	1800	-	ns

#### Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

## ORDERING INFORMATION

ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING
S1xLH	Sub SMA	10,000 / Tape & Reel

#### Notes:

1. "x" defines voltage from 50V(S1ALH) to 1000V(S1MLH)



#### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

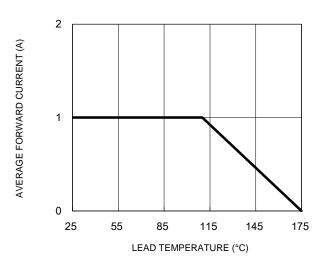
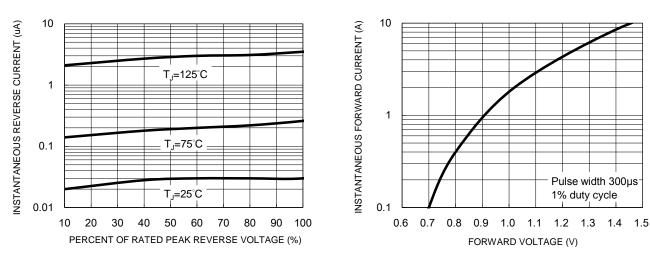
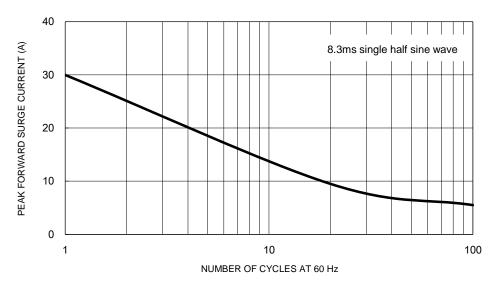


Fig.1 Forward Current Derating Curve

#### **Fig.3 Typical Reverse Characteristics**

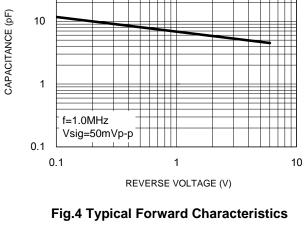


#### Fig.5 Maximum Non-Repetitive Forward Surge Current



**Fig.2 Typical Junction Capacitance** 

100



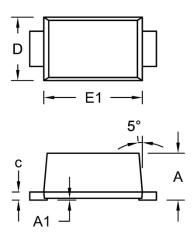
# S1ALH – S1MLH Taiwan Semiconductor

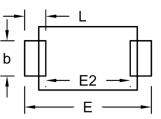


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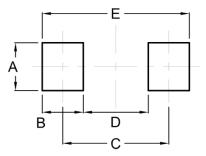
#### Sub SMA





#### Unit (mm) Unit (inch) DIM. Min. Max. Min. Max. Α 1.23 1.43 0.048 0.056 A1 0.00 0.10 0.000 0.004 0.80 1.20 0.031 0.047 b 0.16 0.30 0.006 0.012 С D 1.70 1.90 0.067 0.075 Е 3.40 3.80 0.134 0.150 E1 2.70 2.90 0.106 0.114 E2 2.45 2.60 0.096 0.102 L 0.35 0.85 0.014 0.033

## SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.40	0.055
В	1.20	0.047
С	3.10	0.122
D	1.90	0.075
E	4.30	0.169

### **MARKING DIAGRAM**



P/N	= Marking Code

= Green Compound G

YW = Date Code

F = Factory Code



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